

PTO/SB/08A (08-03)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Application Number	10/660,222
Filing Date	9/10/2003
First Named Inventor:	Gelu Voicu
Examiner: Unknown	GROUP: 223 2829

Sheet 1 of 2 Attorney Docket Number CAT-12502-1

U.S. PATENT DOCUMENTS

Examiner Initials*	Cited No. ¹	Document Number Number - Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Passages or Figures Appear
JL	A01	US-4,271,486	06/02/1981	Dagostino et al.	
	A02	US-4,468,607	08/28/1984	Tanaka et al.	
	A03	US-4,668,932	05/26/1987	Drori et al.	
	A04	US-5,084,667	01/28/1992	Drori et al.	
	A05	US-5,297,056	03/22/1994	Lee et al.	
	A06	US-5,495,245	02/27/1996	Ashe	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document Cntry Code ³ Number ⁴ Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Passages or Figures Appear	T ⁶

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

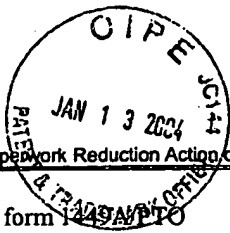
JL	Catalyst Semiconductor, Inc., "CAT515: 8-Bit Quad DACpot with RDY/BUSY and IND. Reference Inputs," 1997, pp. 1-11
	Catalyst Semiconductor, Inc., "CAT524: 8-Bit Quad Digital POT," 1998, pp. 25-36
	Catalyst Semiconductor, Inc., "CAT514: 8-Bit Quad DACpot," 1997, pp. 1-13
	Catalyst Semiconductor, Inc., "CAT514: 8-Bit Quad Digital POT," 1999, pp. 1-12
	Catalyst Semiconductor, Inc., "CAT525: 8-Bit Quad Digital POT With Independent Reference Inputs," 1999, pp. 1-11
	Catalyst Semiconductor, Inc., "CAT5111, CAT5112, CAT5113 and CAT5114: 100/32-Tap Digital POT with Independent Reference Inputs," 1999, pp. 1-6

EXAMINER SIGNATURE: *M. J. Canture*

DATE CONSIDERED: 09/08/04

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	Filing Date	9/10/2003
	First Named Inventor:	Gelu Voicu
	Examiner: Unknown	GROUP: 2838 2879
Sheet 2 of 2	Attorney Docket Number	CAT-12502-1

U.S. PATENT DOCUMENTS

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JL	A07	US-5,717,323		02/1998	Tailliet	
	A08	US-6,201,491	B1	03/13/2001	Brunolli et al.	
	A09	US-6,320,451		11/2001	Harvey et al.	
	A10	US-6,331,768	B1	12/18/2001	Drori et al.	

FOREIGN PATENT DOCUMENTS

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

JL	Stanescu et al., "32 Taps Digitally Controlled Buffered Potentiometer," Proceedings of CAS 2001 International Semiconductor Conference, IEEE Catalog Number 01TH8547, pp. 535-538, October 2001

EXAMINER SIGNATURE: *[Signature]*

DATE CONSIDERED: 09/08/2004

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